

Prior Art Rambus Memory Device

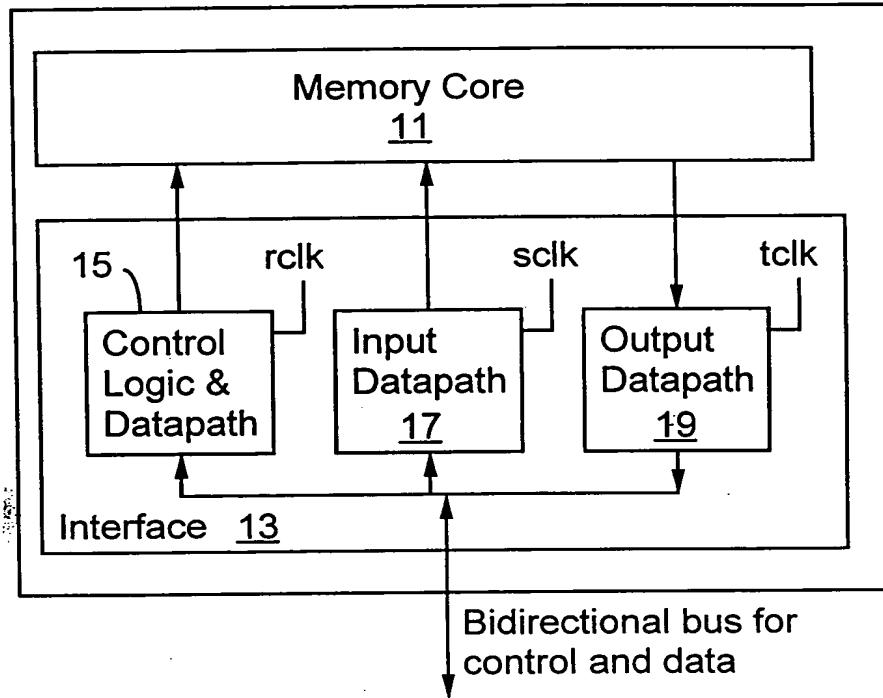


Fig. 1

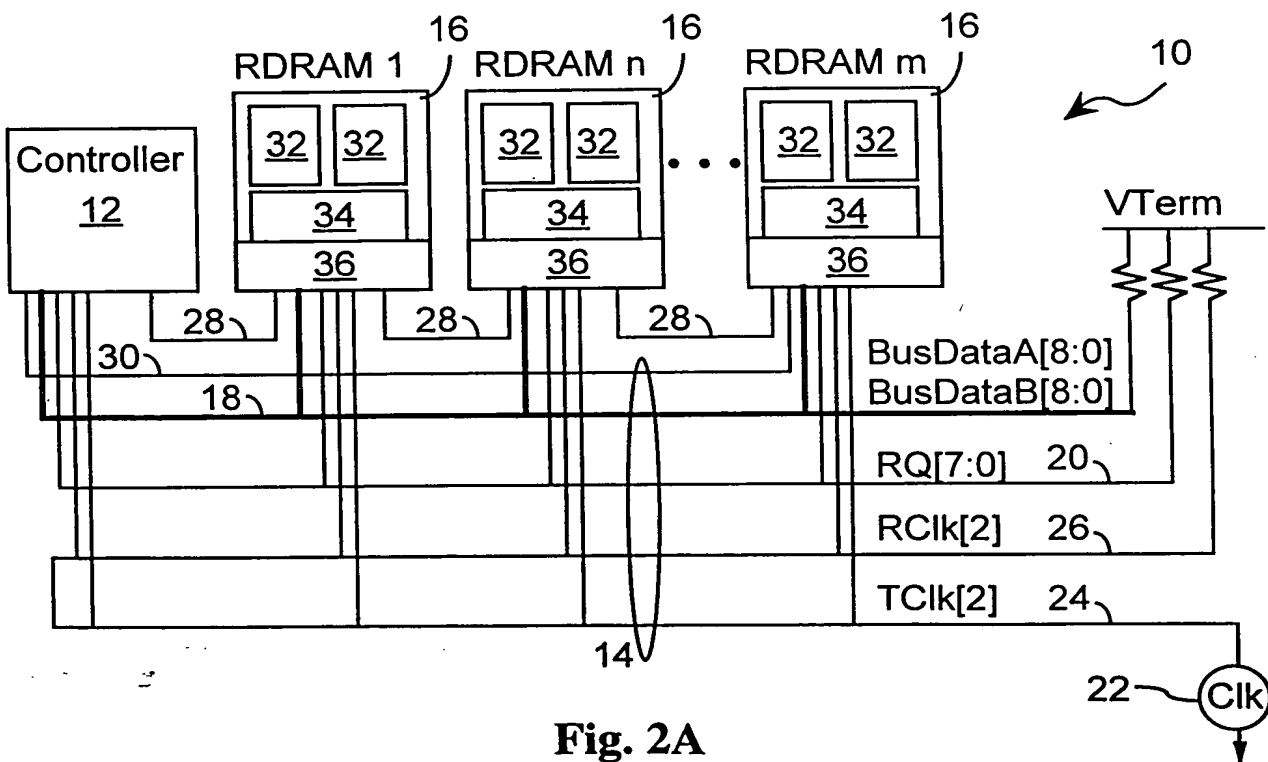


Fig. 2A

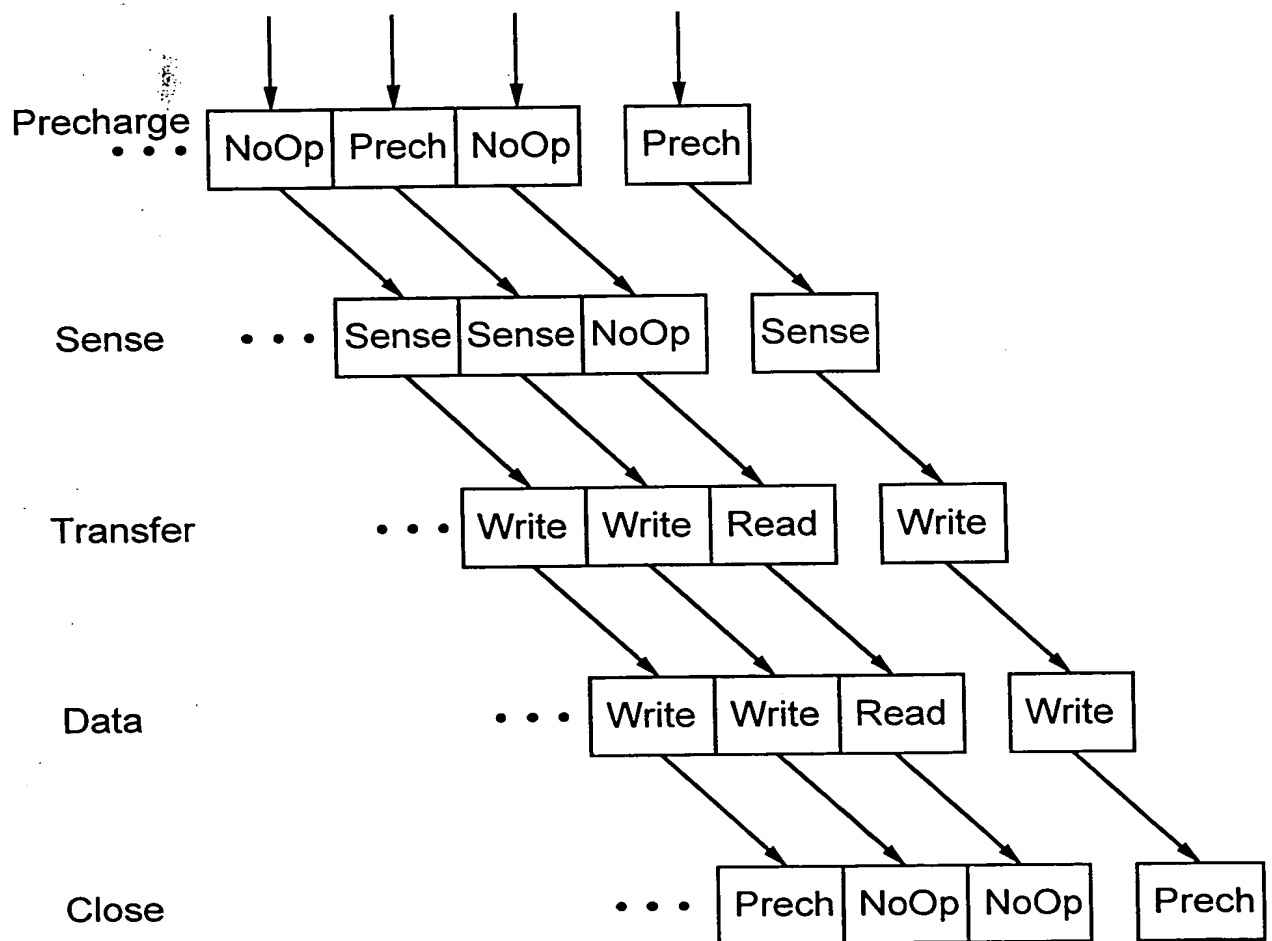


FIG. 2B

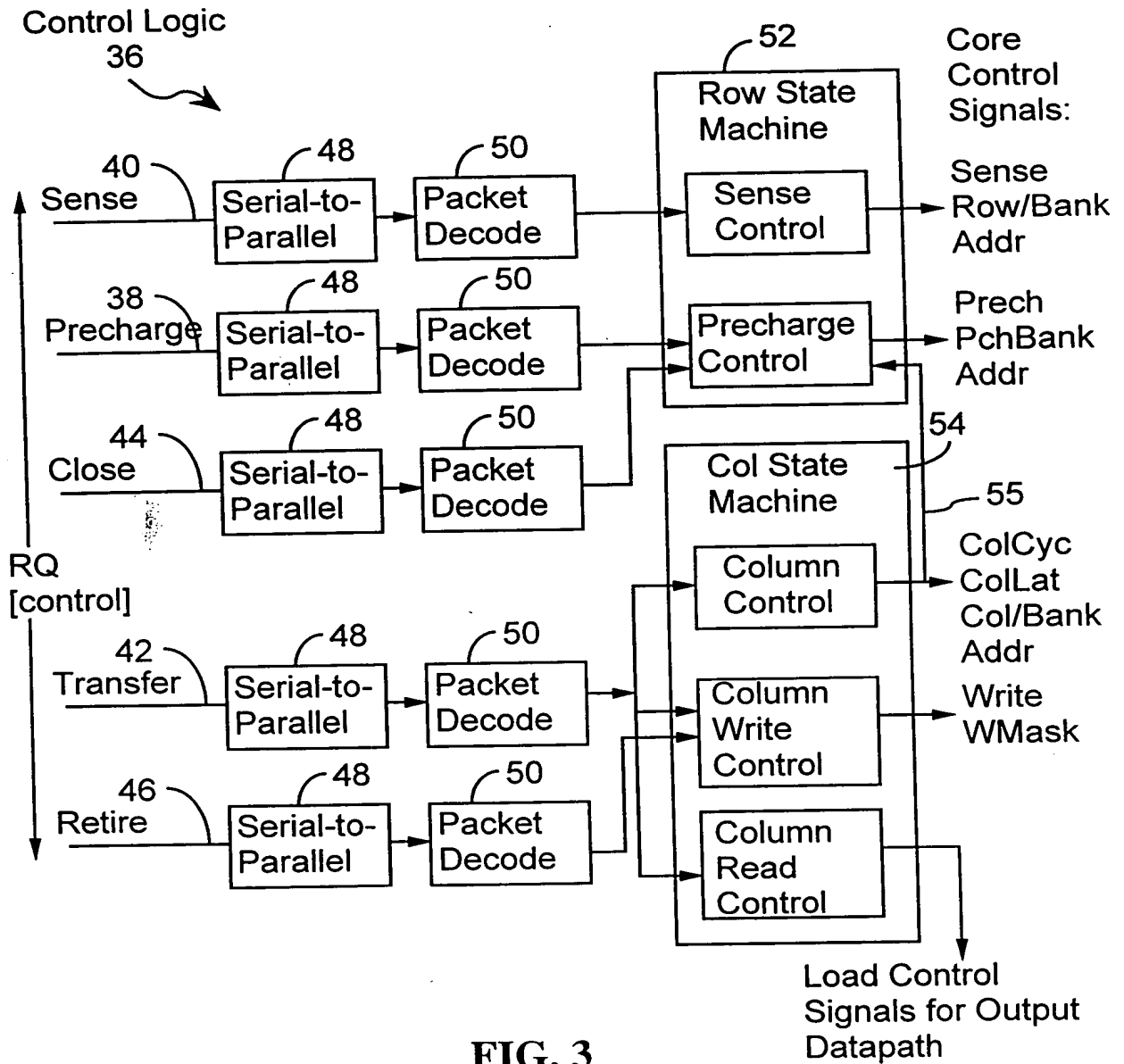


FIG. 3

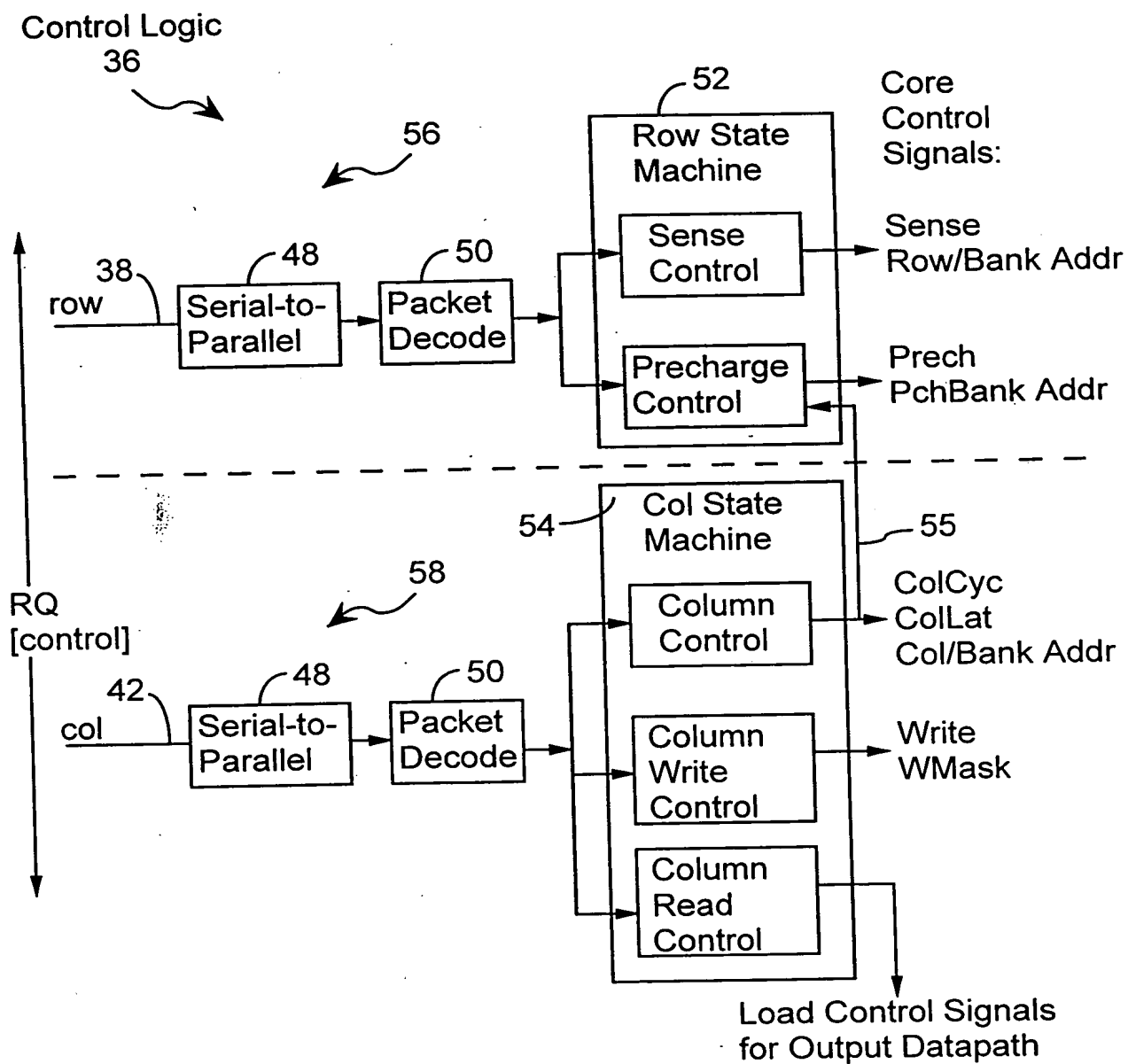


FIG. 4

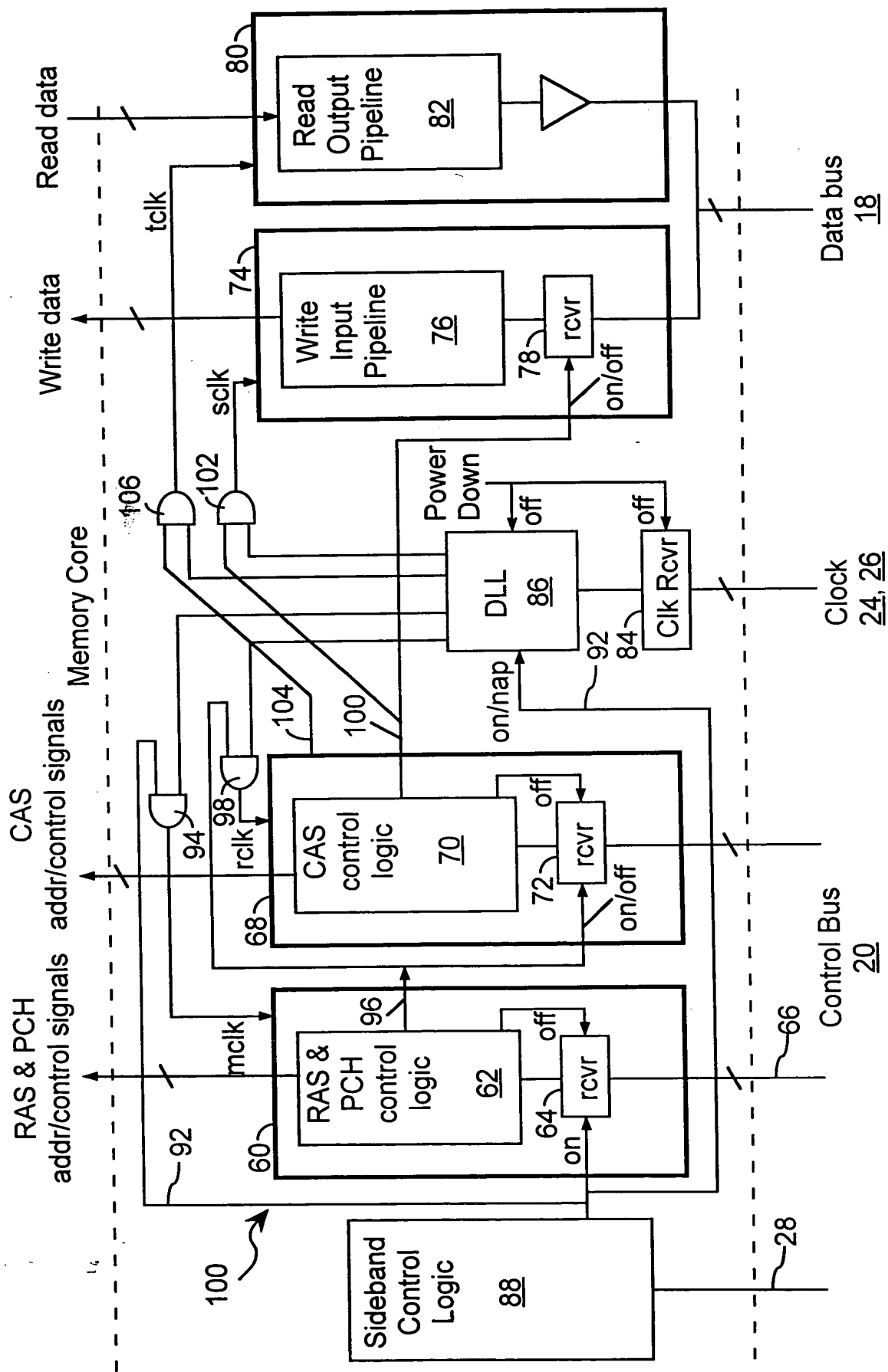


FIG. 5

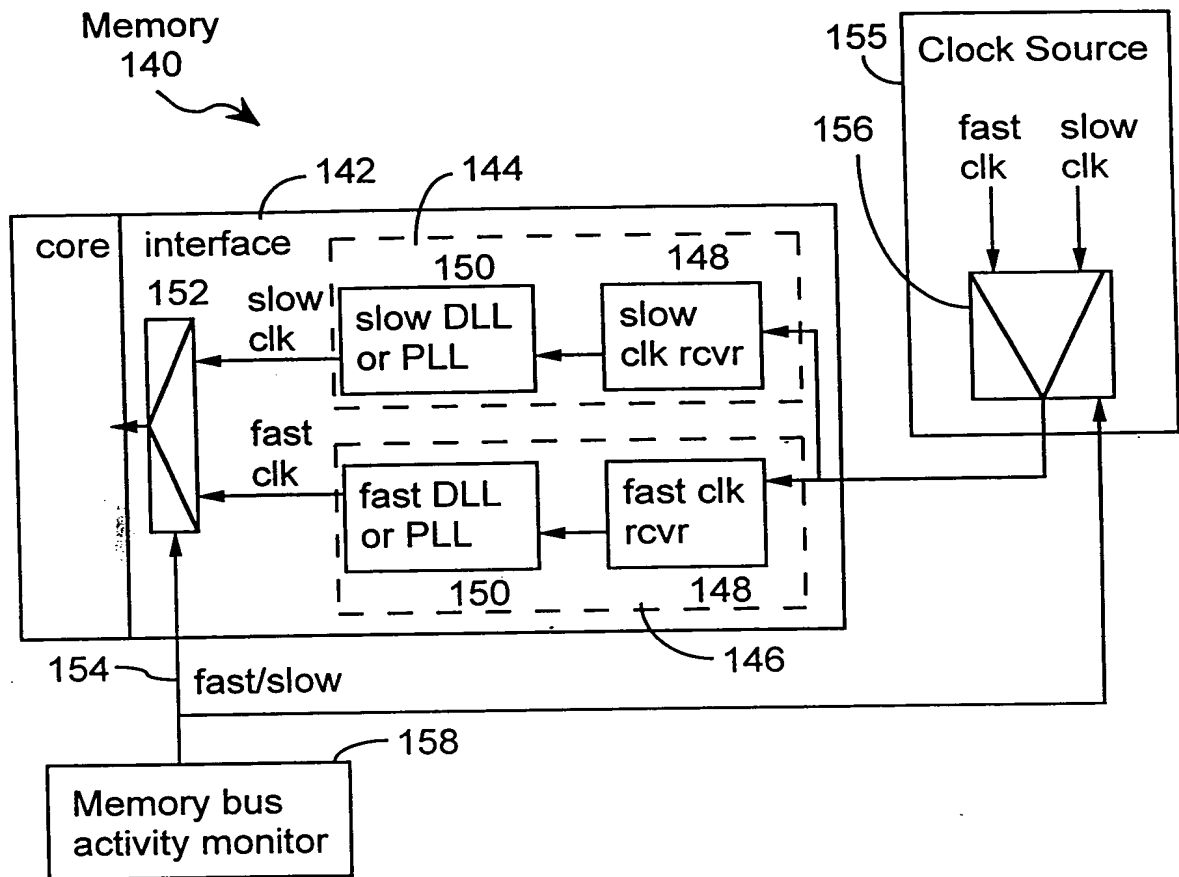


FIG. 7

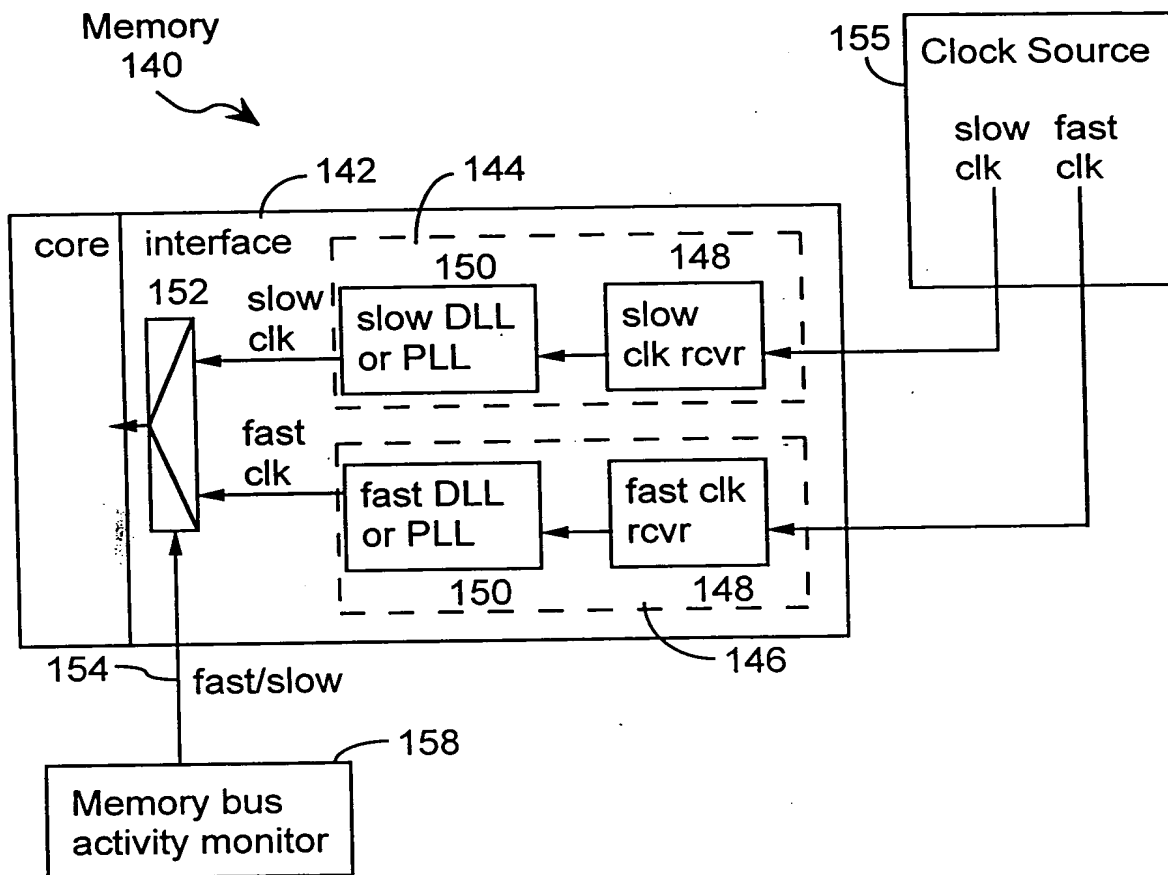


FIG. 8

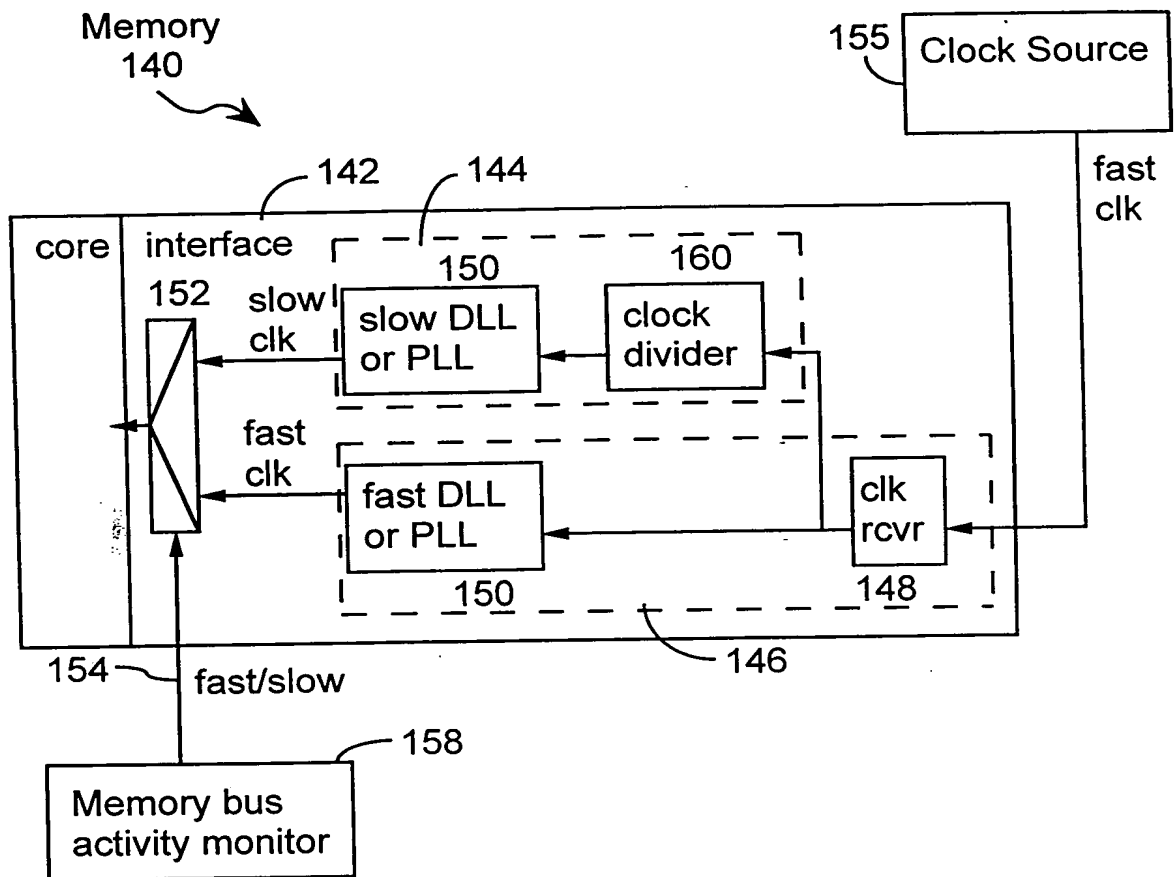


FIG. 9